

## WEST Search History

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DATE: Friday, June 24, 2005

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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L10	L5 and LCR meter	4
<input type="checkbox"/>	L9	L5 and 324/\$.ccls.	12
<input type="checkbox"/>	L8	L5 and channel resistance	52
<input type="checkbox"/>	L7	L5 and error factor	0
<input type="checkbox"/>	L6	L5 and dissipation factor	2
<input type="checkbox"/>	L5	gate capacitance same MOS transistor	796
<input type="checkbox"/>	L4	L2 and 324/\$.ccls.	4
<input type="checkbox"/>	L3	L2 and error factor	0
<input type="checkbox"/>	L2	L1 and MOS transistor	164
<input type="checkbox"/>	L1	(gate capacitance).ab,ti.	1184

END OF SEARCH HISTORY

For 10/784 784